



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Michael Zhuoying Su

Title: INTERCONNECT SPEED SENSING CIRCUITRY

Application No.: 10/700,902

Filed: November 4, 2003

Examiner: Not Yet Assigned

Group Art Unit: 2811

Atty. Docket No.: 1001-0263

February 11, 2004

COMMISSIONER FOR PATENTS

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Alexandria, VA 22313-1450

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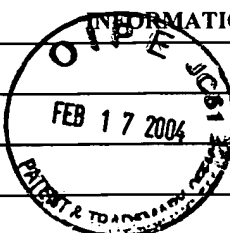
 2/11/04  
Nicole Teitler Cave Date

Respectfully submitted,



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U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 1001-0263		
				Application No.: 10/700,902		
 <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use several sheets if necessary)				Applicant(s): Michael Zhuoying Su		
				Filing Date: November 4, 2003		
				Group Art Unit: 2811		
				Date Submitted: February 11, 2004		
<b>U.S. Patent Documents</b>						
<b>*Examiner Initial</b>		<b>Document Number</b>	<b>Date</b>	<b>Name</b>		
	AA	6,345,209	02-05-2002	Yu		
	AB	6,207,553	03-27-2001	Buynoski et al.		
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					
<b>Foreign Patent Documents</b>						
					<b>Translation</b>	
		<b>Document</b>	<b>Date</b>	<b>Country</b>	<b>Yes</b>	<b>No</b>
	AL					
	AM					
	AN					
	AP					
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
	AR	NEIL H. E. WESTE and KAMRAN ESHRAGHIAN, Principles of CMOS VLSI Design, A Systems Perspective, Second Edition, Addison-Wesley Publishing Co., Copyright 1993 by AT&T, pp. 191-198.				
	AS	TOM PYE and PETER ROSE, Low-Dielectric-Constant Materials for Back-End-of-Line Applications, Semiconductor Fabtech, 8 <sup>th</sup> Edition, pp. 203-207.				
	AT					
<b>Examiner</b>			<b>Date Considered</b>			
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